

# Notice of Allowability

Application No.

10/718,206

Examiner

Dac V. Ha

Applicant(s)

LOKE ET AL.

Art Unit

2611

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to amendment filed on 05/01/07.
2. ☒ The allowed claim(s) is/are 1, 4-19, renumbered as 1-17, respectively.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All   b) ☐ Some\*   c) ☐ None   of the:
    1. ☐ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
  - \* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

## Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08),  
Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

Dac V. Ha  
Primary Examiner  
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***Allowable Subject Matter***

1. This office action is in response to the amendment filed on 05/01/07.
2. Claims 1, 4-19 are allowed.
3. The following is a statement of reasons for the indication of allowable subject matter:

Applicant has cancelled claims 2, 3, 20, 21 and amended claims 1, 4-10 in according to the office action dated 02/10/07. Upon further consideration and comparison with prior art of record (closest reference, Yoshimura et al. – US 5,994,934), amended claims 1, 4-10 are allowed. Claims 11-19 were allowed previously. Particularly, prior art of record, taking individually or collectively, fails to fairly teach a phase-locked loop including “wherein the phase detector system further comprises: a clock control circuit configured to provide a first output signal having a state substantially equal to the state of the first clock and a second output signal having a state substantially equal to the state of the second clock when the input signal has the first state, to provide the first and second output signals having a state asynchronously controlled by the input signal when the input signal has the second state, and to provide a third output signal having a state based on the first and second CP control signals when the input signal has the first state and a state asynchronously controlled by the input signal when the input signal has the second state; and a sequential phase-frequency detector (PFD) configured to provide the first CP control signal having a state based on the states of the first and/or third output signals and the second CP control signal having a state based on the states of the second and/or third output signals”, as

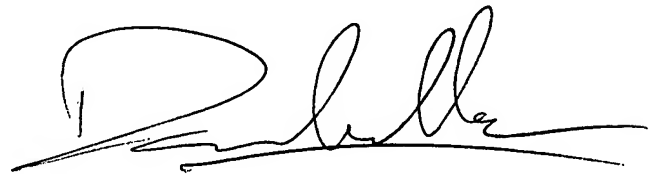
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claimed in independent claim 1 (claims 4-10 depend therefrom); and "a clock control circuit configured to receive a first clock having a state, a second clock having a state, and an input signal defining a plurality of states, to provide a first output signal having a state substantially equal to the state of the first clock and a second output signal having a state substantially equal to the state of the second clock when the input signal has the first state, to provide the first and second output signals having a state asynchronously controlled by the input signal when the input signal has the second state, and to provide a third output signal having a state based on the first and second CP control signals when the input signal has the first state and a state asynchronously controlled by the input signal when the input signal has the second state; and a sequential phase-frequency detector (PFD) configured to provide the first CP control signal having a state based on the states of the first and/or third output signals and the second CP control signal having a state based on the states of the second and/or third output signals", as claimed in independent claim 11 (claims 12-19 depend therefrom). Thus, claims 1, 4-19 are found to be novel and unobvious over prior art of record.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dac V. Ha whose telephone number is 571-272-3040. The examiner can normally be reached on 5/4.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jay Patel can be reached on 571-272-2988. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

A handwritten signature in black ink, appearing to read 'Dac V. Ha', with a horizontal line underneath.

Dac V. Ha  
Primary Examiner  
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